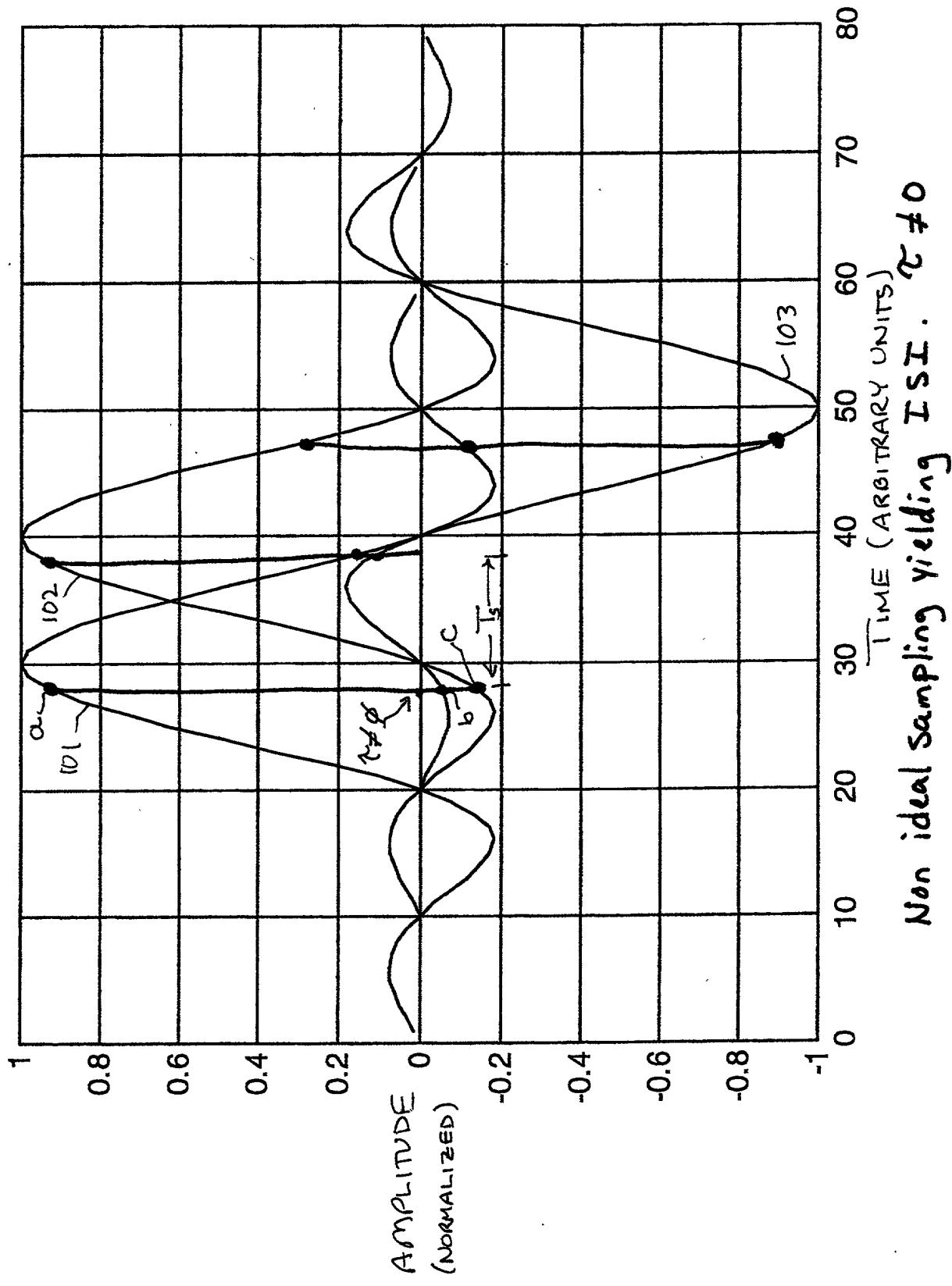


FIG. 1A
(Prior Art)

FIG. 1B
(Prior Act)



Non ideal sampling yielding ISI. $\tau \neq 0$

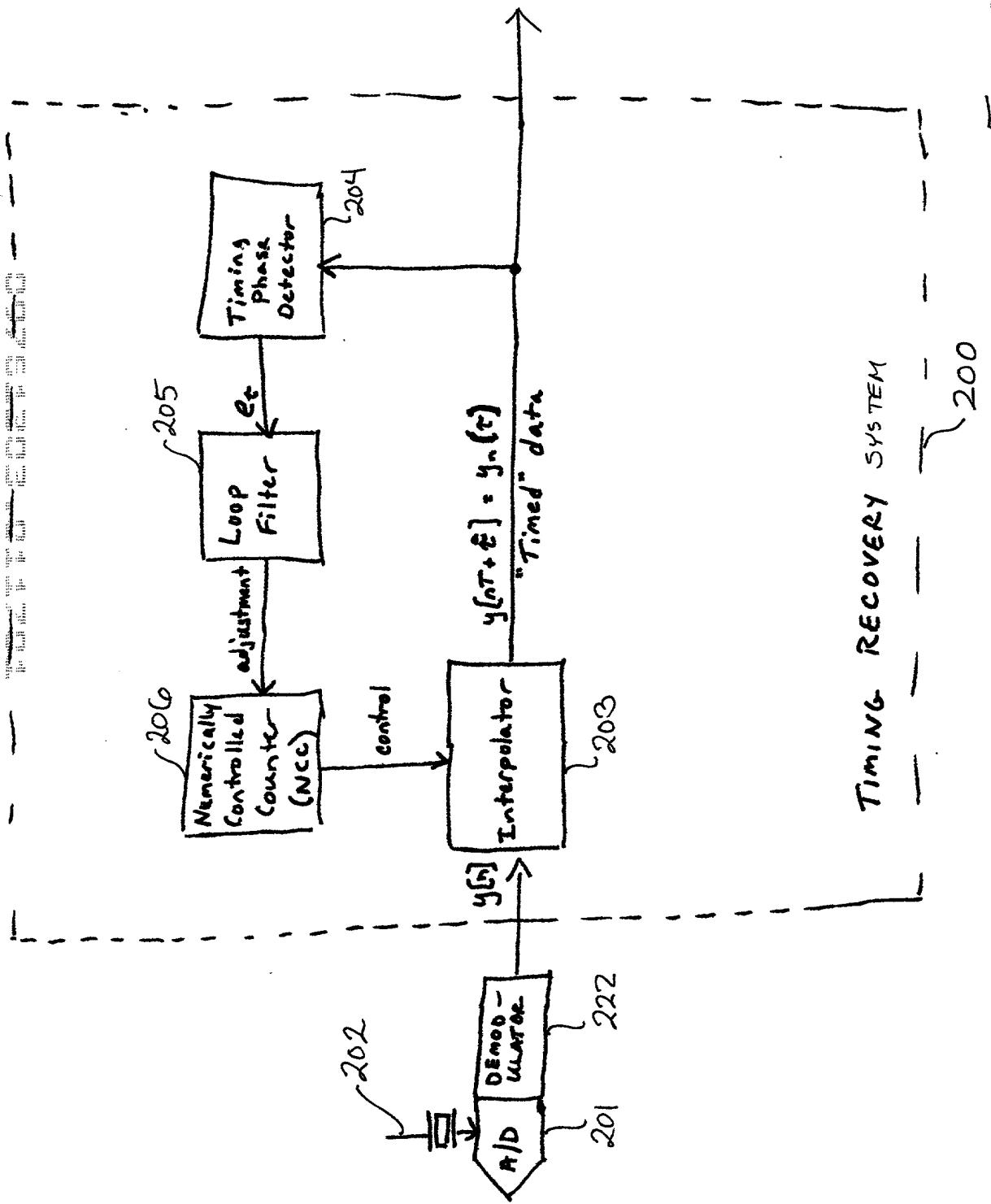


FIG. 2 (Prior Art)

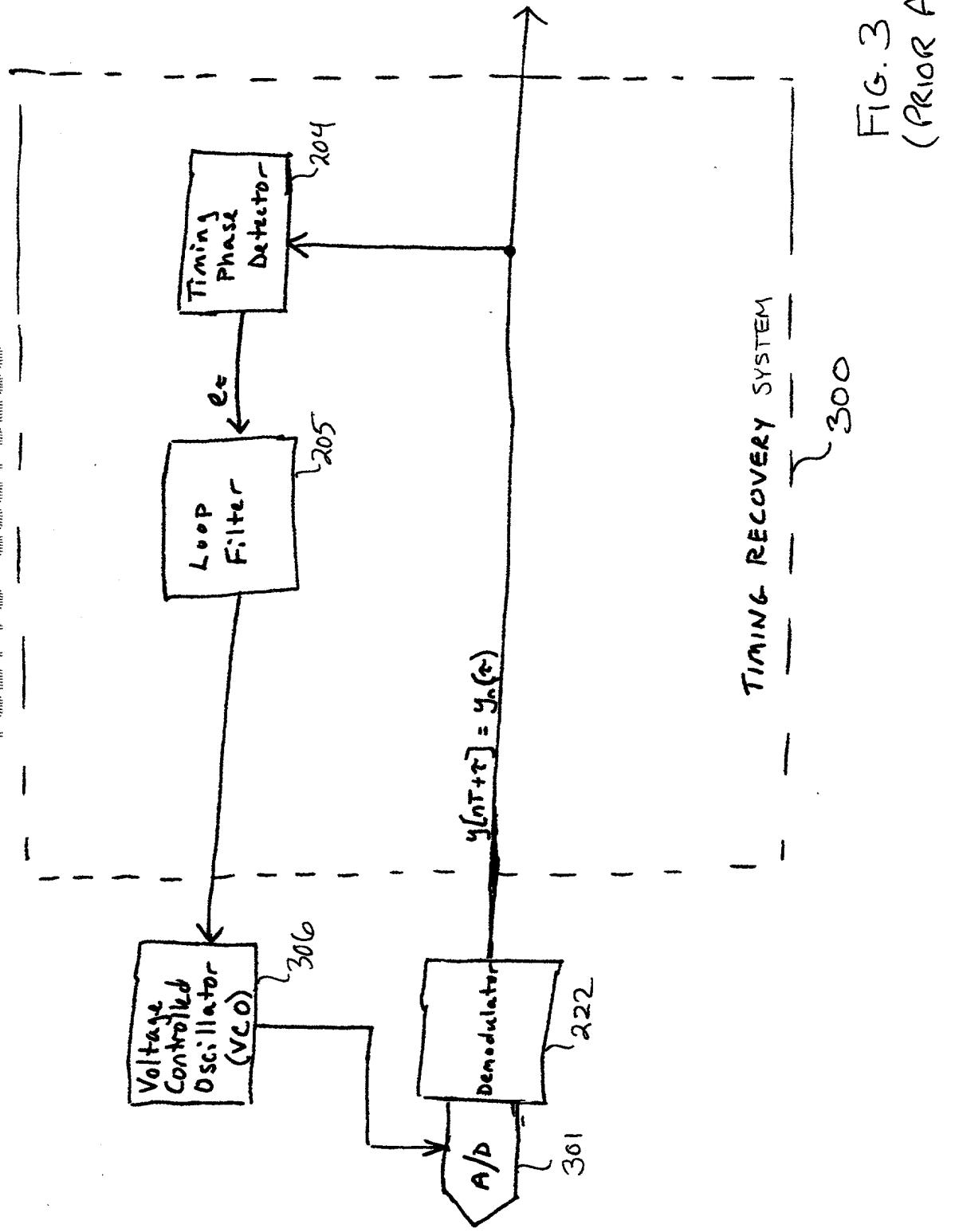
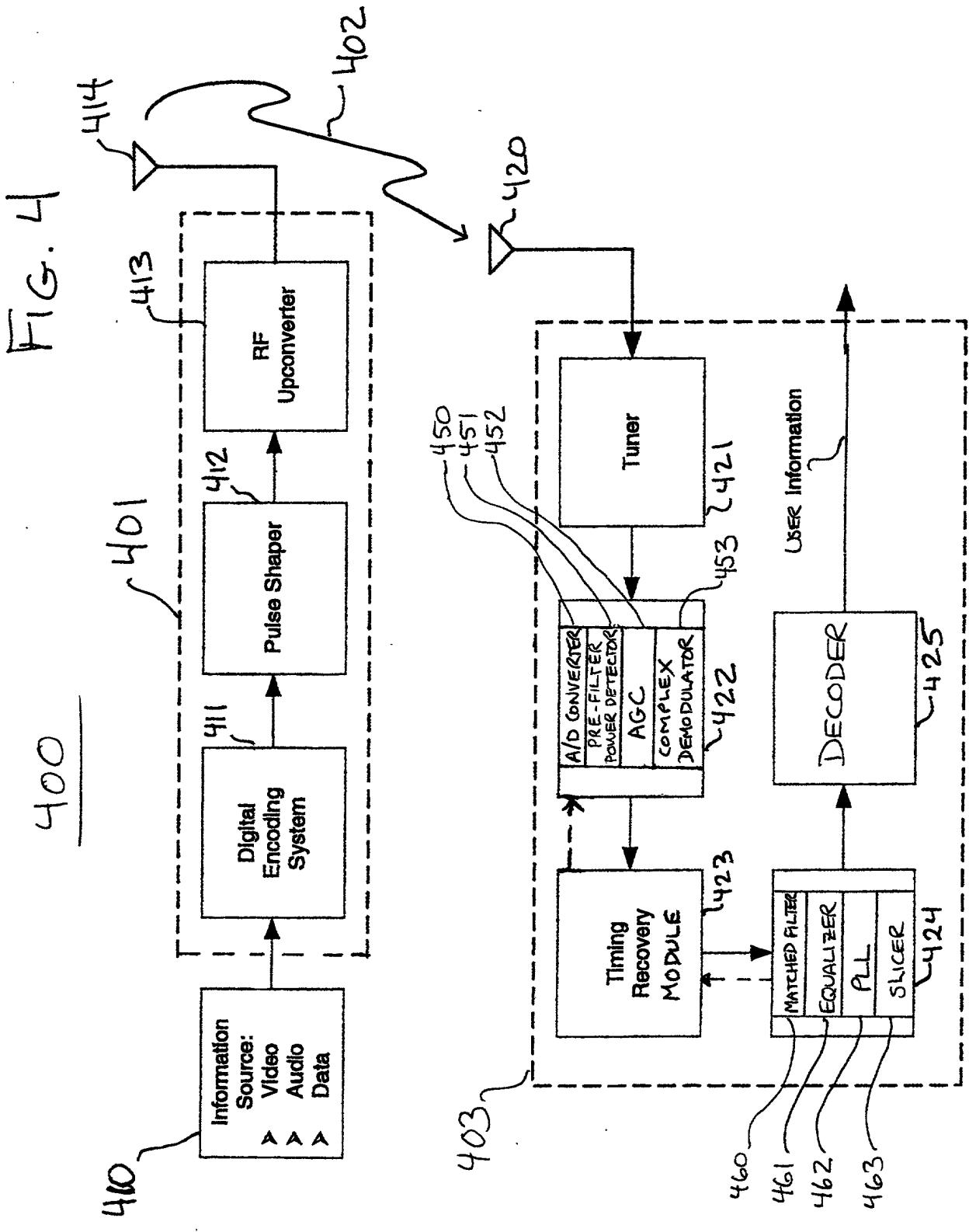


FIG. 3
(PRIOR ART)



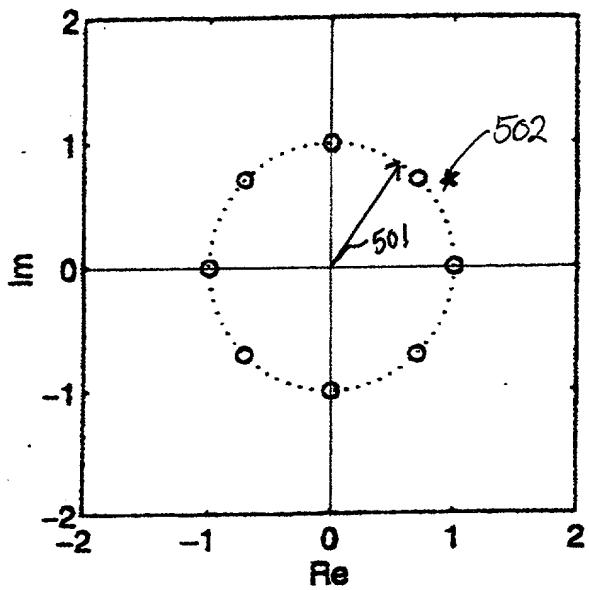


FIG. 5A

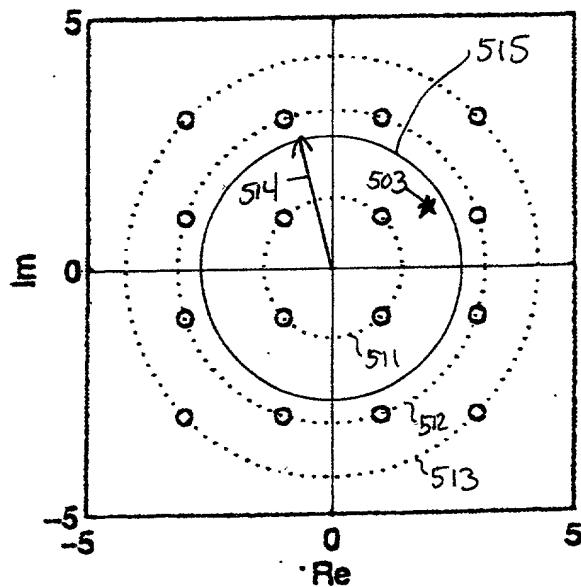


FIG. 5B

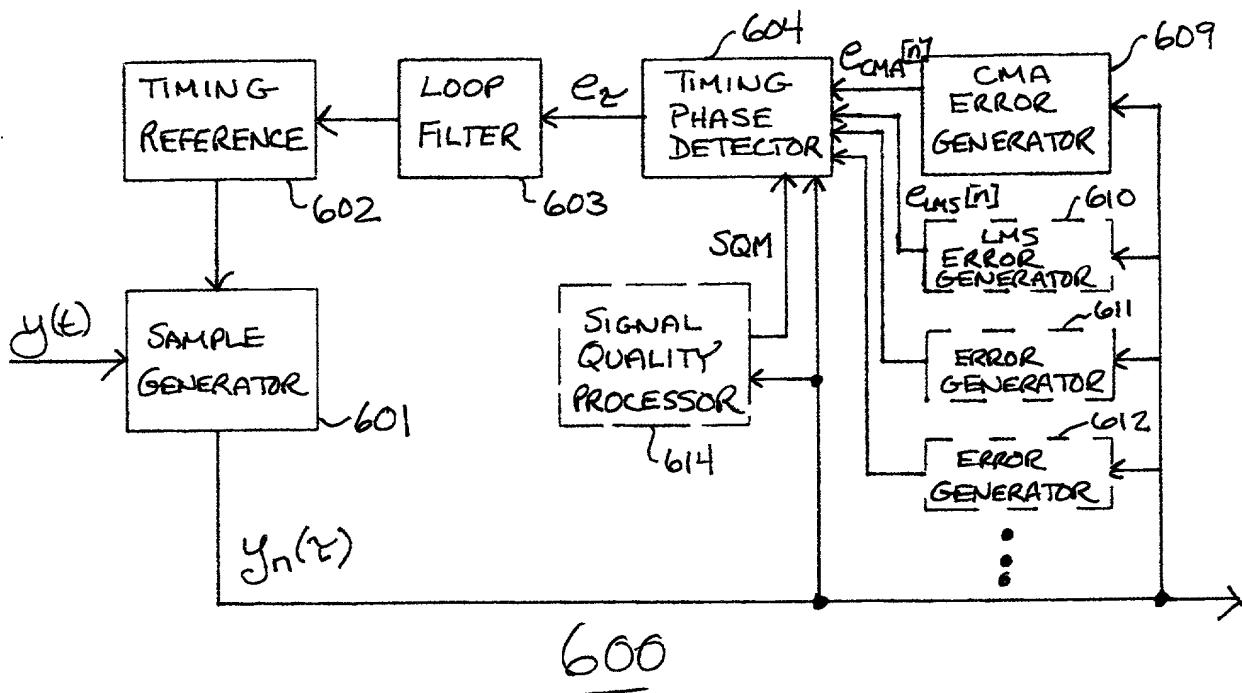
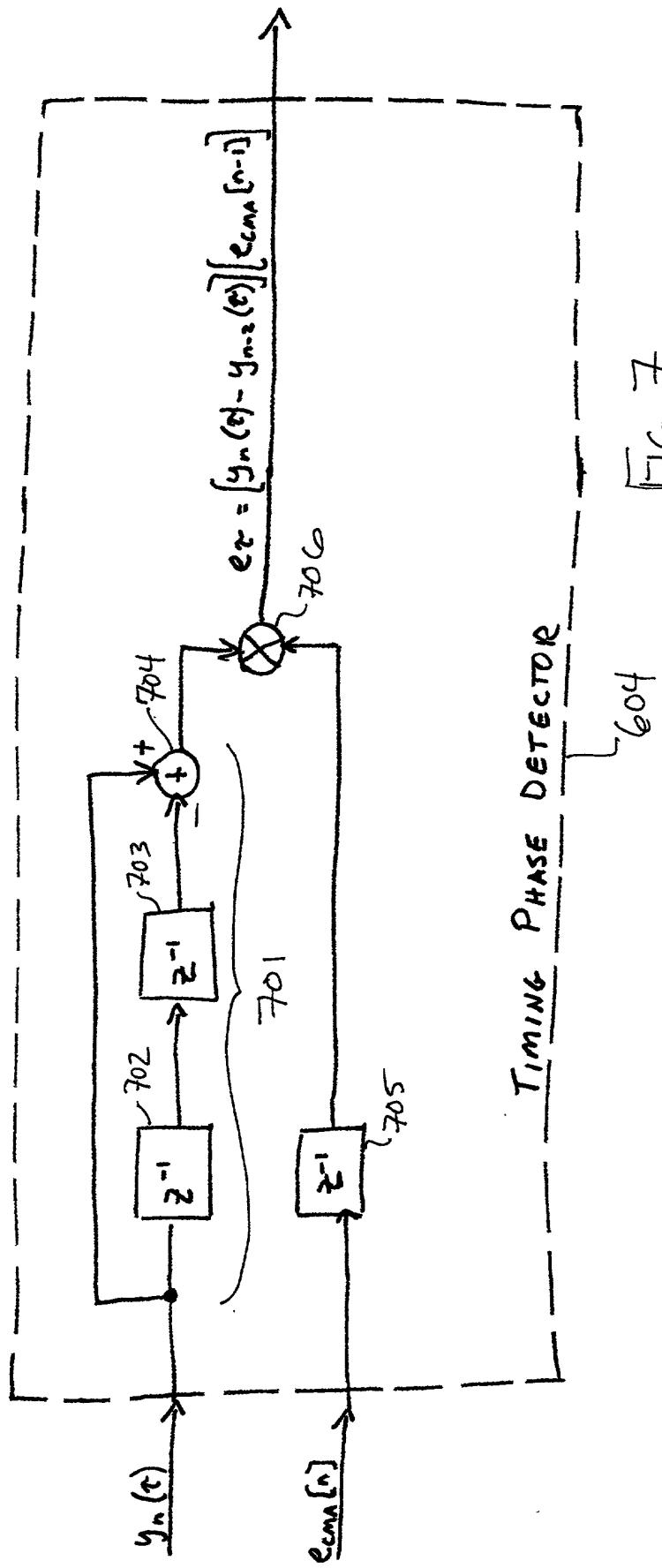


FIG. 6

Fig. 7



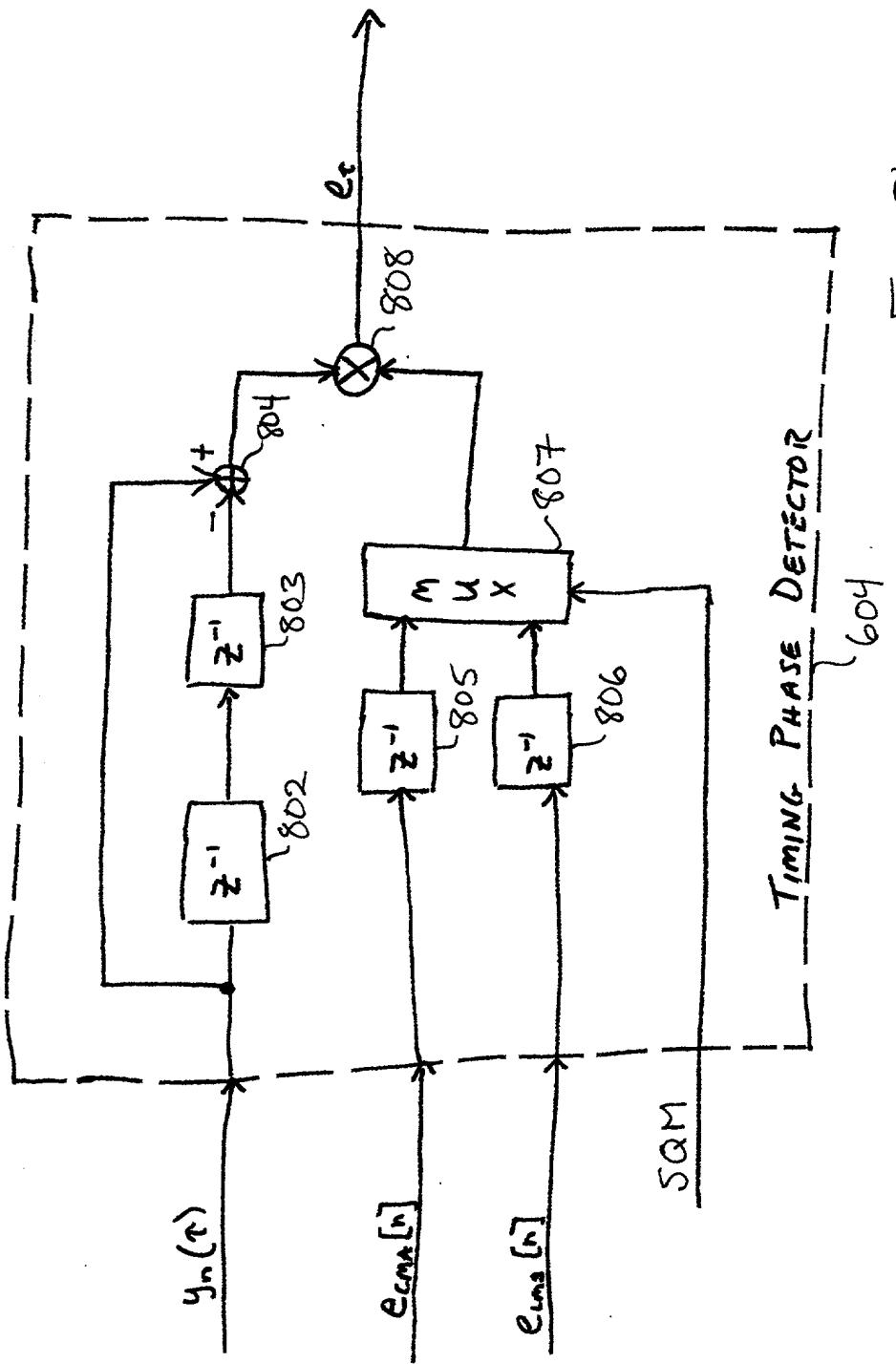


FIG. 8

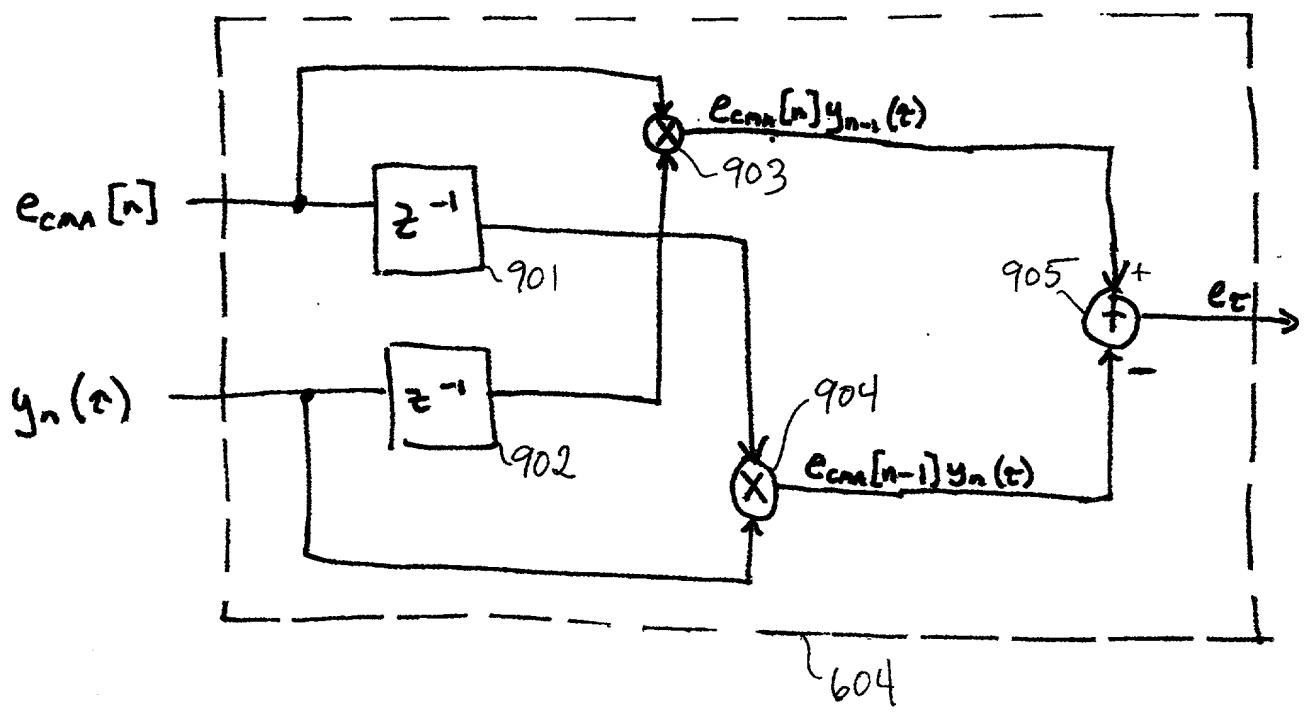


FIG. 9

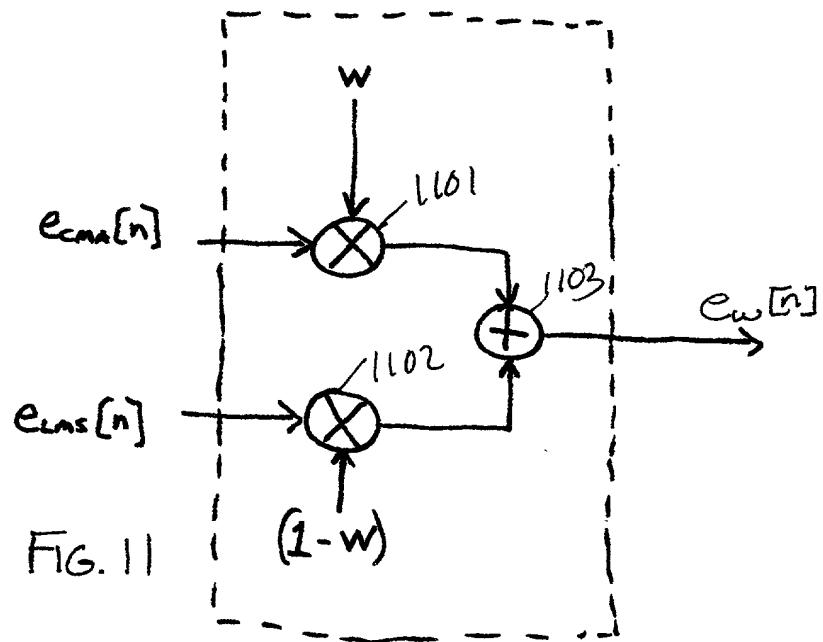


FIG. 11

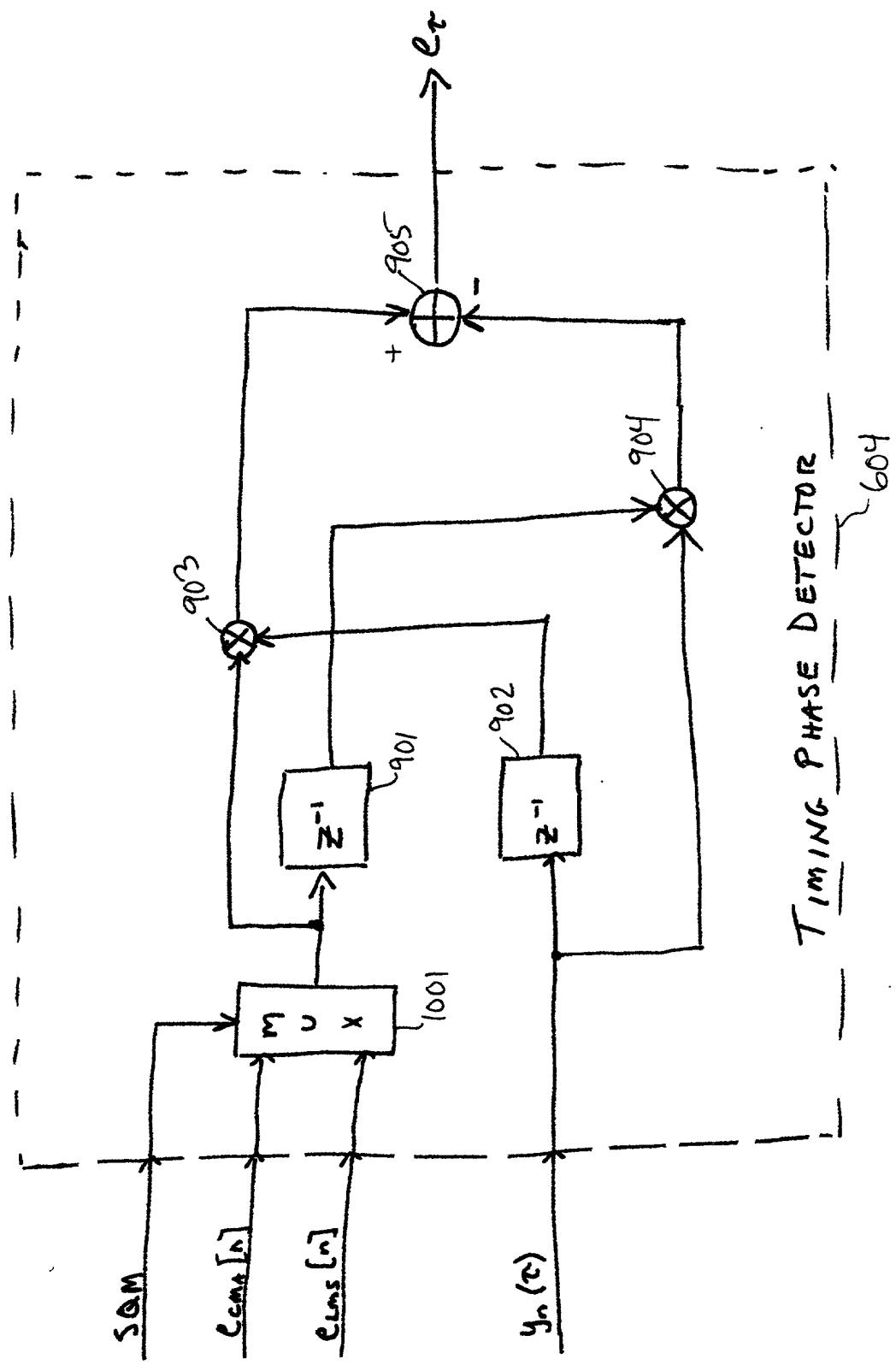
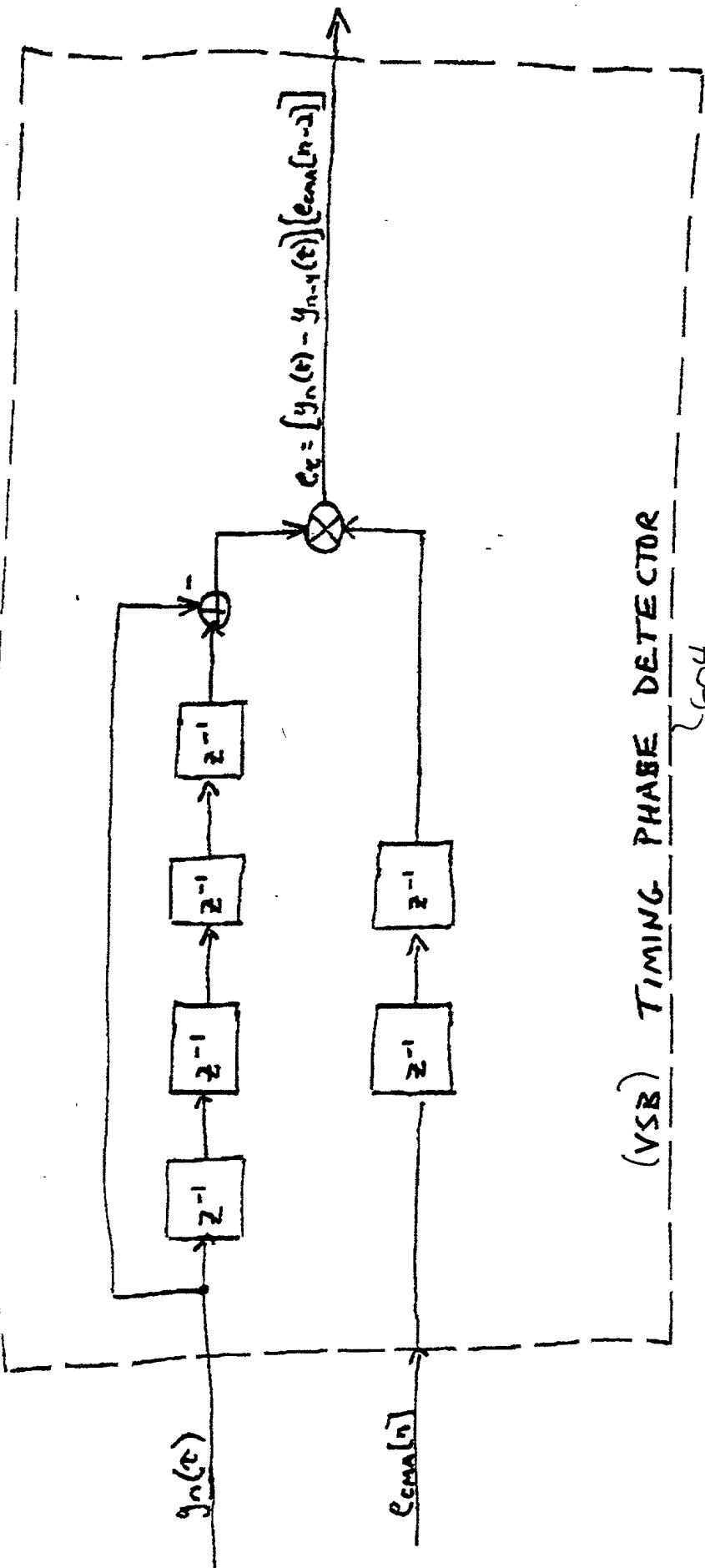


FIGURE 10

FIGURE 12

(VSB) TIMING PHASE DETECTOR

$\{c_0\}$



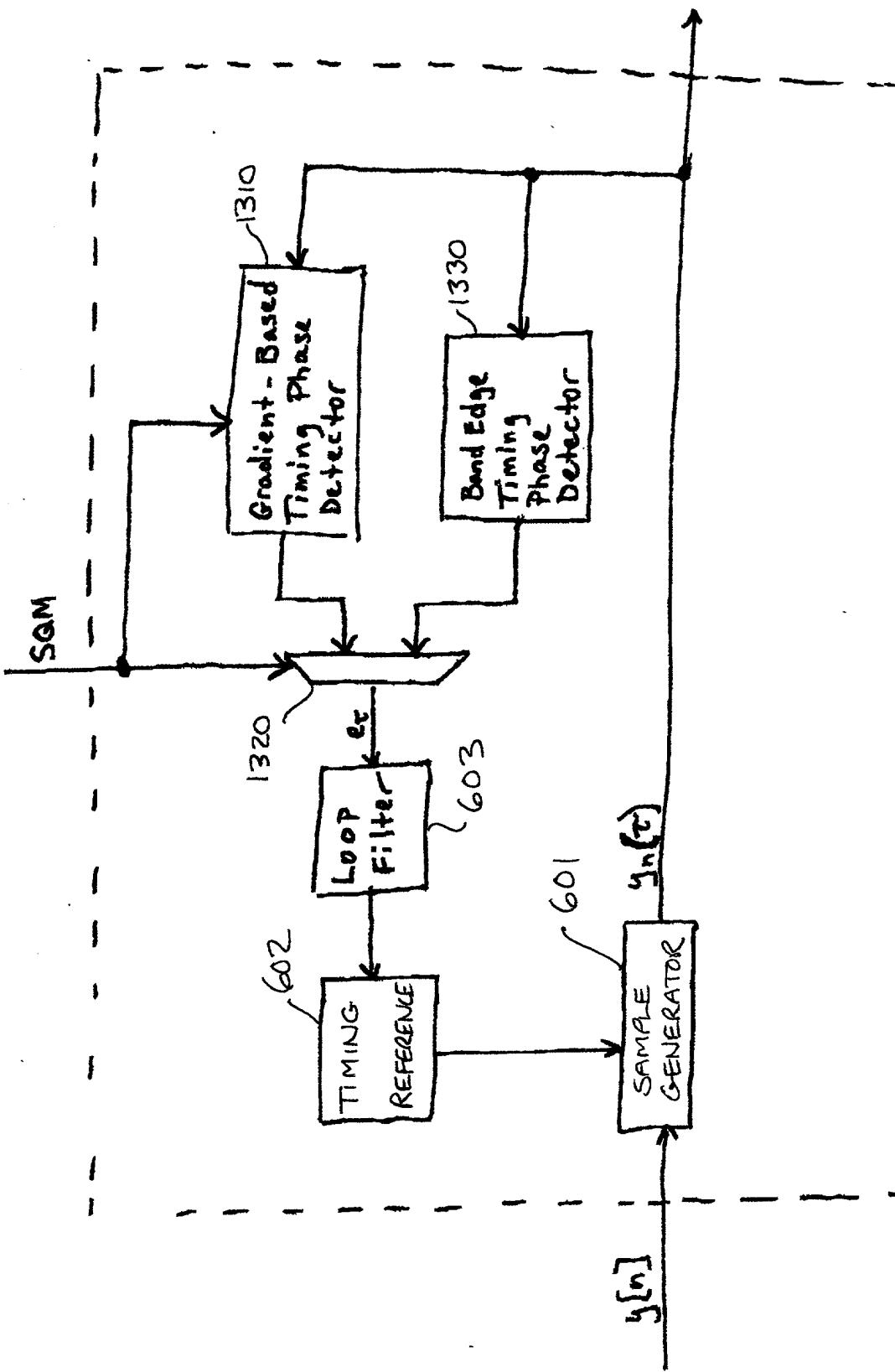


FIG. 13